

Serial No.: 10/826,198
Group Art Unit: 2634

REMARKS

Drawings

The Examiner stated that the drawings are objected to because written labels for components of the invention are needed to clarify the drawings. The Examiner stated that, for example, 218 should have a written term "Dither Unit" along with the numerical number. It was also stated that the objection to the drawings will not be held in abeyance.

The drawing objections are respectfully traversed. The principle elements therein would all be readily understood by those having ordinary skill in the art by reference to the Applicant's detailed description without the need for labeled textual descriptions in the drawings.

Also, labeled textual descriptions are not required under 37 CFR §1.74, which specifies that "...the detailed description of the invention shall refer...to the different parts by use of reference letters or numerals (preferably the latter)."

Since the USPTO is a PCT receiving office and is bound by PCT Rule 11.11, which states that "...drawings shall not contain text matter, except a single word or words, when absolutely indispensable...", and since such textual matter is clearly not absolutely indispensable in the present drawings, withdrawal of the objections in compliance with controlling PCT Rule 11.11 is respectfully requested.

Claim Rejections - 35 USC §103

Claims 1 and 4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kelkar et al. (US Patent No. 5,663,991, hereinafter "Kelkar") in view of Yanagisawa et al. (US Patent No. 6,528,982, hereinafter "Yanagisawa").

Kelkar provides an integrated circuit chip having built-in self-measurement for phase lock loop output clock jitter and phase error. An edge sorting circuit measures jitter between corresponding transition edges of a measured clock and a reference clock. A decoder circuit reads in the value and increments a corresponding counter. A state machine then reads the counters, processes the information, and outputs one or more PLL clock error values.

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Conclusion

In view of the above, it is submitted that the claims are in condition for allowance and reconsideration of the rejections is respectfully requested. Allowance of claims 1-20 at an early date is solicited.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including any extension of time fees, to Deposit Account No. 50-0374 and please credit any excess fees to such deposit account.

Respectfully submitted,



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